

Docket No.: P2001,0377

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : JUDITH MAGET

Filed : CONCURRENTLY HEREWITH

Title : INTEGRATED, TUNABLE CAPACITOR

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 6,100,770 (Litwin et al.), dated August 8, 2000;

U.S. Patent No. 5,965,912 (Stolfa et al.), dated October 12, 1999;

U.K. Patent Application GB 2 138 206 A (Sakai), dated October 17, 1984;

Patent Abstracts of Japan 03147376 A (Hidetomo), dated June 24, 1991;

European Patent Application EP 1 024 538 A1 (Manzini et al.), dated August 2, 2000;

European Patent Application EP 0 902 483 A1 (Litwin et al.), dated March 17, 1999;

Burghartz, J. N. et al.: "Integrated RF and Microwave Components in BiCMOS Technology", IEEE Transactions on Electron Devices, Vol. 43, No. 9, September 1996, pp. 1559-1570;

Maget, J. et al.: "A Varactor with High Capacitance Tuning Range in Standard 0.25µm CMOS Technology", 4 pages;

Wong, W. M. Y. et al.: "A Wide Tuning Range Gated Varactor", IEEE Journal of Solid-State Circuits, Vol. 35, No. 5, May 2000, pp. 773-779;

Svelto, F. et al.: "A Three Terminal Varactor for RFIC's in Standard CMOS Technology", IEEE Transactions on Electron Devices, Vol. 47, No. 4, April 2000, pp. 893-895;

Tiebout, M.: "A Fully Integrated 1.3GHz VCO for GSM in 0.25 μ m Standard CMOS with a Phasenoise of -142Bc/Hz at 3MHz Offset", European Microwave Week, 2000, 4 pages;

Andreani, P. et al.: "On the Use of MOS Varactors in RF VCO's", IEEE Journal of Solid-State Circuits, Vol. 35, No. 6, June 2000, pp. 905-910;

Porret, A.-S. et al.: "Design of High-Q Varactors for Low-Power Wireless Applications Using a Standard CMOS Process", IEEE Journal of Solid-State Circuits, Vol. 35, No. 3, March 2000, pp. 337-345;

International Search Report, dated November 29, 2002.

As per the Notice in 1273 OG 55 (August 5, 2003) no copies of any above-mentioned U.S. patents and U.S. patent application publications are submitted for any application filed after June 30, 2003.

Respectfully submitted,

LAURENCE A. GREENBERG
REG. NO. 29,308



For Applicant

Date: December 1, 2003

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/nt/kf

FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: P2001,0377 Appl. No.: <hr/> Applicant: JUDITH MAGET <hr/> Filing Date: December 1, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	6,100,770	8/8/00	Litwin et al.			
	B	5,965,912	10/12/99	Stolfa et al.			
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FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	2 138 206 A	10/17/84	U.K.			
	K	03147376 A	6/24/91	Japan			
	L	1 024 538 A1	8/2/00	Europe			
	M	0 902 483 A1	3/17/99	Europe			
	N						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
		Burghartz, J. N. et al.: "Integrated RF and Microwave Components in BiCMOS Technology", IEEE Transactions on Electron Devices, Vol. 43, No. 9, September 1996, pp. 1559-1570					
		Maget, J. et al.: "A Varactor with High Capacitance Tuning Range in Standard 0.25um CMOS Technology", 4 pages					
EXAMINER				DATE CONSIDERED			
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